

L Number	Hits	Search Text	DB	Time stamp
-	94	bridge same flush same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 09:09
-	23	(bridge same flush same buffer) and compaq	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 11:13
-	67	bridge same (memory adj read) same multiple same line	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 09:10
-	3	bridge same (memory adj read) same multiple same line same buffer\$3 same flush\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 09:10
-	13	((memory adj3 read) same prefetch\$3 same flush\$3 same bridge)	USPAT	2004/05/26 09:46
-	0	pci adj to adj pci adj bridge	USPAT	2004/05/26 09:50
20794	2	pci-to-pci adj bridge	USPAT	2004/05/26 09:50
-	147	(pci-to-pci adj bridge) same read same prefetch\$3	USPAT	2004/05/26 09:51
-	27	(pci-to-pci adj bridge) same read same prefetch\$3 same flush\$3	USPAT	2004/05/26 09:52
-	7	(pci adj3 bridge) same read same prefetch\$3 same flush\$3	USPAT	2004/05/26 09:52
-	6	(pci adj bridge) same read same prefetch\$3 same flush\$3	USPAT	2004/05/26 10:57
-	14	(scsi adj disk adj controller) same pci same bridge	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 10:53
-	30	((memory adj read) and (memory adj read adj multiple) and (memory adj read adj line)) adj command	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 11:29
-	61	(cache adj line\$1) same (four adj times)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/26 17:23
-	4639	(memory read) same (four adj times)	USPAT	2004/05/26 17:28
-	45	(memory read) same multiple same line same (four adj times)	USPAT	2004/05/26 17:28
-	0	(prefetch adj size) same (four adj times)	USPAT	2004/05/26 17:34
-	3	(prefetch adj size) same (read near3 (multiple line))	USPAT	2004/05/26 17:37
-	0	(secondary adj cache) same (read near3 multiple) same advantage\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 10:18
-	63	(secondary adj cache) same advantage\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 10:24
-	76	(secondary adj cache) same efficien\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 10:24

-	0	(secondary adj cache) same efficien\$3 same (multiple near3 read)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 10:25
-	0	(secondary adj cache) same efficien\$3 same (read near (cache adj line\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 10:26
-	424	(cache adj memory) same advantage\$1 same efficien\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 12:06
-	3	(cache adj memory) same advantage\$1 same efficien\$3 same ((secondary external) adj cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 10:27
-	7	(SECONDARY ADJ cache) same advantage\$1 same efficien\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 12:08
-	76	(SECONDARY ADJ cache) same efficien\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 12:38
-	40	bridge same (memory adj read adj multiple) same cache	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 12:40
-	14	cache same bridge same (read near multiple near command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:54
-	1	(read adj multiple adj command) same (cache adj memory) same bridge	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 15:28
-	4	bridge same ((prefetch pre-fetch\$3) adj buffer\$3) same flush\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 17:02